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Master's Degree in
Microelectronic
Design



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I am a graduate engineer from the École Nationale Supérieure d'Électronique et de Radioélectricité de Grenoble (ENSERG, now known as PHELMA) in France, where I graduated in 2003.

I started my career at STMicroelectronics, where I was involved in the design of non-volatile memory circuits. Later, I had the opportunity to work at Chipidea, where I specialised in the design of power management circuits, including LDO, DC-DC, references, among others. I am currently part of the ams OSRAM team, where I play the role of technical lead and designer of mixed-signal integrated circuits. My areas of expertise include power management, SAR ADCs, inductive proximity sensors and temperature sensors. My current focus is on the development of sensors for tomography. Throughout my career, I have had the honour of being the author of 7 patents



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